Form PTO-1449 (modifications Proceedings of Patents and Publications  For Applicant's Information  Disclosure Statement NOV 2 4 2003						SERIAL NO. 09/841,912 GROUP: 2123	
(U	se severa	al sheets if necessary)	JULING DA	TE: April 24, 2001 DOCUMENTS			
						CI ID	FILING DATE
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	APPROPRIAT
					-		
					REC	EW	<del>)                                    </del>
					NOV	2 8 2003	
					Tachnolo	gy Center	aû0
					סוטווויפטן	97 00	
FOREIGN PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO YES/NO
	C1	99/66554	12/23/99	wo			
				\		-	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
<b>PF</b>	C2	International search report application number PCT/US02/12707 mailed September 24, 2003					
RF	C3	"High Frequency Model for Power Electronics Capacitors", Siami, et al, <u>IEEE Transactions on Power Electronics</u> , March 2001, IEEE, US, Vol 16, No. 2, pages 157-166.					
Œ	C4	"A Study of the High Frequency Performance of Thin Film Capacitors for Electronic Packaging", Chen, et al, IEEE Transactions on Advanced Packaging, May 2000, IEEE, US, Vol 23, No. 2, pages 293-302.					
RF	C5	"A High Frequency Model Based on the Physical Structure of the Ceramic Multilayer Capacitor", de Vreede, et al, <u>IEEE Transactions on Microwave Theory and Techniques</u> , July 1992, IEEE, US, Vol 40, No. 7, pages 1584-1587.					
RF	C6	"Distributed SPICE Circuit Model for Ceramic Capacitors", Smith, et al, 2001 Proceedings, 51st Electronic Components and Technology Conference 2001, Orlando, FL, May 29-June 1, 2001, pages 523-528					

EXA	MIN	ER:
-----	-----	-----

KUSSELL	L
EUS>ELL	[ Kt //_>

DATE CONSIDERED:

9.19.04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Form PTO-1449 (modified)
List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

ATTY. DKT. NO. 5181-84400

FEB 2 5 2002

APPLICANT: Smith at al.

SERIAL NO. 09/841,912

**GROUP: 2123** 

FILING DATE: April 24, 200 EUS

		U.	S. PATENT	DOCUMENTS			
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE APPROPRIAT
			··	ORCO TO THE PARTY OF THE PARTY	_		<del> </del>
				To T			
			<u> </u>	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		<del></del>	
		-		180		<del>-</del>	
·····					1	<del></del>	
				REC	EIV	ED_	
				MAR Technolo	0 4 20	72	
			<del> </del>	Technolog	OV Centa	r 0100	
	-				3) OCING	12100	
	_	FORI	EIGN PATE	ENT DOCUMENTS			-
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO YES/NO
					-		
		OTHER ART (Ir	icluding Auth	or, Title, Date, Pertinent Pag	es, Etc.)		L
				Down to Rules of Thumb", Gree		June 5, 2000	- / ).
<del></del>							

EXA	MIN	ICD.
レヘハ	TALL.	VER.

RUSSEU FREJD

DATE CONSIDERED:

9,19.04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

MON 3 0 50501

Form PTO-1449 (modified)
List of Patents and Publications For Applicant's Information Disclosure Statement

**SATTY. DKT. NO. 5181-84400** 

SERIAL NO. 09/841,912

APPLICANT: Smith et al.

GROUP: 2123

(Use several sheets if necessary)			FILING DA	ATE: April 24, 2001			
U.S. PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS		FILING DATE IF APPROPRIATE
RE	A1	5,883,814	3/1999	Luk et al.	716	2	06 ,CV
RF	A2	6,252,760	6/2001	Sen	361	306,30	OF CRITE
							63 m
							100
	<b>_</b>						
	ļ					ų	
	ļ						
	<u> </u>						
	<u> </u>					_	
	<u> </u>	FOR!	EIGN PATE	NT DOCUMENTS			1
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
					_		
	-						
		OTHER ARE					<u> </u>
0-	1 4 2			or, Title, Date, Pertinent I			FE 166
RF	A3						
RF	A4	Chen et al., "On-Chip Decoupling Capacitor Optimization for High-Performance VLSI Design," May 1995, pp. 99-103.					
RF-	A5	Drewniak, "Modeling Power Bus Decoupling on Multilayer Printed Circuit Boards," August 1994, pp. 456-461.					

EXAMINER:			
	TV	1	ATT D

**DATE CONSIDERED:** 

9,19.04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Best Available Copy

Information Disclosure Statement--PTO 1449 (modified)